

## LATCH-UP TESTING REQUEST FORM

(Please fill the information in the white fields, double click the small box and check it to mark your selection)

|  |  |  |  |
| --- | --- | --- | --- |
| **Company**: |  | **Location:** |  |
| **Contact Name:** |  | **Email:** |  |
| **Phone No:** |  | **Date:** |  |
| **Part Number:** |  | **Lot#/Date Code:** |  |

|  |  |  |
| --- | --- | --- |
| 1. Sample Size: |  | JEDEC std: 6 units |
| 2. Are vectors required? | Yes No | (If ”Yes”, please provide vector pattern) |
| 3. Package Type: |  |
| 4. Package Pin/Ball Pitch: |  |
| 5. Is Package drawing provided? | Yes No |
| 6. Total number of device pins: |  |
| 7. Total number of no connect pins: |  |
| 8. Please provide detail device netlist/pin list (**see Table 1, page 2** for required information, file can be sent as an excel workbook). | | |
| 9. Is elevated Temperature required? | Yes No | If “Yes”, please specify: \_\_ °C |

#### POSITIVE/NEGATIVE CURRENT LATCH-UP:

|  |
| --- |
| 10. Please fill out below table with **internally** **separated voltage(s)** and its corresponding current when device is setup per netlist provided. |

**Supply and Current info:**

|  |  |  |  |
| --- | --- | --- | --- |
| **Power Supply** | **Supply Pin Name** | **Max Operating Power Supply Level (V)** | **Supply Current (mA)** |
| Vcc1 |  |  |  |
| Vcc2 |  |  |  |
| Vcc3 |  |  |  |

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 11. Any Power Sequencing? | | | | | | | Yes No | | | | | If “Yes” please describe (ie. Vcc2, Vcc3, Vcc1, etc.) | | | | |
|  | | | | | | | | | | | | | | | | |
| 12. Does device require preconditioning?  (ie. Resetting the device after power-up) | | | | | | | Yes No | | | | | If “Yes” please describe in detail below. | | | | |
|  | | | | | | | | | | | | | | | | |
| 13. | Starting trigger current is | |  | | mA, increase in | | | | |  | | | mA steps to a maximum current of | |  | mA |
| The trigger voltage should be limited to | | | | | | |  | V | | | | | | | |
| Set trigger pulse duration to | | |  | | mS (JEDEC 78 recommends 10us to 5s). | | | | | | | | | | |
| The wait time before ICC will be measured is | | | | | | | |  | | mS (JEDEC 78 recommends 3mS to 5s). | | | | | |
| 14. Latch-up is defined as | | **1.4xInom or Inom+10 mA**  **whichever is greater (JEDEC 78)** | | | | | | | | | | | | If different please specify below. | | |
|  | | | | | | | | | | | | | | | | |

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#### SUPPLY OVER-VOLTAGE TEST:

|  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 15. | Power device to: |  | | x1.5 V (VccQ = maximum operating voltage) | | | | | | | | |
| 16. | Starting trigger voltage is |  | | V, increase in | | | |  | | V steps to a maximum voltage of |  | V |
| The trigger current should be limited to | | | | |  | mA | | | | | |
| Set trigger pulse duration to | |  | | mS (JEDEC 78 recommends 10us to 5s). | | | | | | | |
| The wait time before ICC will be measured is | | | | | |  | | mS (JEDEC 78 recommends 3mS to 5s). | | | |